

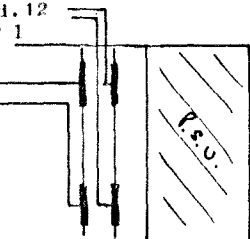


BOARD NUMBER	12	EPL NUMBER	CE05/01, CE02/02, 03, 05.	CATALOGUE NUMBER	B1630								
<u>Discription</u>	CHANNEL ADAPTOR Board. For use on 2050 systems only to extend the Normal Interface and Store Interface from the C.P. into an extension crate. To be used in conjunction with Bd.13 (Or Bd.120 if link to SAD) <b>Note</b> Under special system requirements may be found in 4000 series crate e.g. CAMAC												
<u>Position</u>	Normally the lowest numbered slot. Position 5 if fitted in 2050 C.P.												
<u>Patching</u>	READY		WAY										
Ready/Way	<table border="1"> <tr><th colspan="2">Court Area</th></tr> <tr><td>Y2</td><td></td></tr> </table>		Court Area		Y2		<table border="1"> <tr><th colspan="2">Court Area</th></tr> <tr><td></td><td></td></tr> </table>			Court Area			
Court Area													
Y2													
Court Area													
Other	NONE												
<p>N.B. Boards 12 and 13 MUST be patched to the SAME Ready No.</p> <p>Typical Config.</p>													
<u>EDGE CONNECTERS</u>													
EC1	EC2	EC3	EC4	EC5	EC6								
			To EC4 of Bd.13		to EC6 of Bd. 13								
<u>Alternatives</u>													

BOARD NUMBER 17	EPL NUMBER CE02/05	CATALOGUE NUMBER B1635													
<p><u>Description</u> PERIPHERAL ADAPTOR LOCAL LINK BOARD. For use on 2050 systems in extension units with more than one link fitted. Used in conjunction with Channel Adaptor Bd.12. Bd.17 is an unterminated Bd.13 (See info on Bd.13)</p> <p>No longer a Company product and unlikely to be encountered in the field.</p>															
<p><u>Position</u> Fitted in lowest numbered available slot.</p>															
<p><u>Patching</u> Ready/Way</p>	<p style="text-align: center;">READY</p> <table border="1" style="margin-left: auto; margin-right: auto;"> <tr><td colspan="2" style="text-align: center;">Court Area</td></tr> <tr><td style="width: 50px; height: 50px;"></td><td style="text-align: center; vertical-align: middle;">Y6</td></tr> </table>		Court Area			Y6	<p style="text-align: center;">WAY</p> <table border="1" style="margin-left: auto; margin-right: auto;"> <tr><td colspan="2" style="text-align: center;">Court Area</td></tr> <tr><td style="width: 50px; height: 50px;"></td><td style="width: 50px; height: 50px;"></td></tr> </table>	Court Area							
Court Area															
	Y6														
Court Area															
<p><u>Other</u></p>	<p>Set switch in area B9 as shown.</p> <div style="display: flex; justify-content: space-around;"> <div style="text-align: center;">  <p>1 5 6 8</p> <p>CLOSED, as set when used in a normal extension unit with only one link.</p> </div> <div style="text-align: center;">  <p>1 4 6 8</p> <p>OPEN, as set when more than 1 link is fitted to the same ext unit. Each PA local link will have its enable switch set to OPEN. Ext switches are fitted to EC5 of PA link in these cases.</p> </div> </div>														
<div style="display: flex; align-items: center;"> <div style="margin-right: 20px;"> <p>To Bd.12 From CP 1</p> <p>To Bd.12 From cp 2</p> </div>  <div style="margin-left: 20px;"> <p>← Ext. Crate</p> <p>← Typical configuration</p> </div> </div> <p style="text-align: center;">17 13</p>															
<p><u>EDGE CONNECTERS</u></p>															
<table border="1" style="width: 100%; text-align: center;"> <tr> <td style="width: 16.6%;">EC1</td> <td style="width: 16.6%;">EC2</td> <td style="width: 16.6%;">EC3</td> <td style="width: 16.6%;">EC4</td> <td style="width: 16.6%;">EC5</td> <td style="width: 16.6%;">EC6</td> </tr> <tr> <td></td> <td></td> <td></td> <td>To Channel Adaptor EC4</td> <td></td> <td>To Channel Adaptor EC6</td> </tr> </table>				EC1	EC2	EC3	EC4	EC5	EC6				To Channel Adaptor EC4		To Channel Adaptor EC6
EC1	EC2	EC3	EC4	EC5	EC6										
			To Channel Adaptor EC4		To Channel Adaptor EC6										
<p><u>Alternatives</u> See info on Bd.13</p>															

BOARD NUMBER	42	EPL NUMBER	PT10/02,PT02/03,PT04/05/01		CATALOGUE NUMBER	B1660								
<u>Description</u>	TELEPRINTER COMPATIBLE DEVICE CONTROLLER board. May be used on 2050 or 4000 series machines. Normally used as a controller for TEXAS Thermal Printer but is also suitable for any TTY compatible device, e.g. Hazeltine, Data Dynamics Tty....etc.													
<u>Position</u>	Any Normal Interface slot.													
<u>Patching</u>	READY		WAY											
Ready/Way	<table border="1"> <tr><td colspan="2">Court Area</td></tr> <tr><td>Y2 or Y3</td><td>Y6</td></tr> </table>		Court Area		Y2 or Y3	Y6	<table border="1"> <tr><td colspan="2">Court Area</td></tr> <tr><td>Y5</td><td>Y8</td></tr> </table>				Court Area		Y5	Y8
Court Area														
Y2 or Y3	Y6													
Court Area														
Y5	Y8													
Other	Two further patches will be found in court areas Y9 & Y10 to set up the Baud rate of the board for the device connected. See below A D.I.L. switch in area A5 also has to be set to suit the device in use.													
<u>examples</u>		<u>Y9</u>	<u>Y10</u>		<u>A5 DIL Switch</u>									
TELEPRINTER		0	1	(110 Baud)										
THERMAL PRINTER		8	5	(300 Baud)										
HAZELTINE 1000		15	5	(2400 Baud)										
<u>EDGE CONNECTERS</u>														
	EC1	EC2	EC3	EC4	EC5	EC6								
					connected to device.									
<u>Alternatives</u>														
FOR DATA DYNAMICS TELETYPE ONLY Board 26														

BOARD NUMBER 100	EPL NUMBER ALL 4080 & 4070		CATALOGUE NUMBER B1718													
<u>Discription</u> ROM 1024 Board. For use on all 4000 series systems except 4060. This board contains 56 x1024 PROM elements which form the CPU microprogram. The outputs are used to control the data flow through the 4080 register structure. The inputs are generated on ROM address board 101 and are used to select the next microprogam address.																
<u>Position</u> Dedicated to POSITION 1 of the Central Processor crate.																
<u>Patching</u> Ready/Way   Other	READY <table border="1" data-bbox="656 576 892 715"> <tr> <td colspan="2">Court Area</td> </tr> <tr> <td></td> <td></td> </tr> </table>		Court Area				WAY <table border="1" data-bbox="1098 576 1328 715"> <tr> <td colspan="2">Court Area</td> </tr> <tr> <td></td> <td></td> </tr> </table>		Court Area							
	Court Area															
Court Area																
None																
<u>EDGE CONNECTERS</u> <table border="1" data-bbox="455 1270 1278 1437"> <tr> <td>EC1</td> <td>EC2</td> <td>EC3</td> <td>EC4</td> <td>EC5</td> <td>EC6</td> </tr> <tr> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> </tr> </table>					EC1	EC2	EC3	EC4	EC5	EC6						
EC1	EC2	EC3	EC4	EC5	EC6											
<u>Alternatives</u> NONE																

BOARD NUMBER	101	EPL NUMBER	ALL 4080 & 4070			CATALOGUE NUMBER	31719															
<u>Discription</u>	ROM ADDRESS 1024 BOARD. For use on ALL 4000 range systems except 4060. This board provides the next microprogram address to ROM 1024 Bd.100																					
<u>Position</u>	Dedicated to POSITION 2 of the CPU crate.																					
<u>Patching</u>	READY		WAY																			
Ready/Way	<table border="1"> <tr> <td>Court</td> <td>Area</td> </tr> <tr> <td></td> <td></td> </tr> </table>		Court	Area			<table border="1"> <tr> <td>Court</td> <td>Area</td> <td></td> <td></td> <td></td> <td></td> </tr> <tr> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> </tr> </table>				Court	Area										
Court	Area																					
Court	Area																					
Other	None																					
<u>EDGE CONNECTERS</u>																						
<table border="1"> <tr> <td>EC1</td> <td>EC2</td> <td>EC3</td> <td>EC4</td> <td>EC5</td> <td>EC6</td> </tr> <tr> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> </tr> </table>							EC1	EC2	EC3	EC4	EC5	EC6										
EC1	EC2	EC3	EC4	EC5	EC6																	
<u>Alternatives</u>	NONE																					

BOARD NUMBER	102	EPL NUMBER	ALL 4080 & 4070	CATALOGUE NUMBER	B1720
<u>Description</u>					
CONTROL 'A' BOARD. For use on ALL 4000 series systems except 4060. This board contains the Main Register read controls, Shifter Control, Exponent Arithmetic control control together with the micropogram format decode and a small amount of other control logic.					
<u>Position</u>					
Dedicated to POSITION 3 of the Central Processor crate.					
<u>Patching</u>					
Ready/Way	None	READY		WAY	
		Court	Area	Court	Area
Other		None			
<u>EDGE CONNECTERS</u>					
EC1	EC2	EC3	EC4	EC5	EC6
<u>Alternatives</u>					
NONE					

BOARD NUMBER	103	EPL NUMBER	ALL 4080 & 4070				CATALOGUE NUMBER	B1721							
<u>Discription</u>		CONTROL 'B' BOARD. For use on ALL 4000 series systems excluding 4060. This board contains the Conditions control logic, function unit control, M2 control and multiplication and division logic,													
<u>Position</u>		Dedicated to POSITION 4 of the Central Processor crate.													
<u>Patching</u>		READY		WAY											
Ready/Way	None	<table border="1"> <thead> <tr> <th>Court</th> <th>Area</th> </tr> </thead> <tbody> <tr> <td></td> <td></td> </tr> </tbody> </table>		Court	Area			<table border="1"> <thead> <tr> <th>Court</th> <th>Area</th> </tr> </thead> <tbody> <tr> <td></td> <td></td> </tr> </tbody> </table>		Court	Area				
Court	Area														
Court	Area														
Other	None														
<u>EDGE CONNECTERS</u>															
	EC1	EC2	EC3	EC4	EC5	EC6									
<u>Alternatives</u>		NONE													

BOARD NUMBER	104	EPL NUMBER	ALL 4080 & 4070	CATALOGUE NUMBER	B1722								
<u>Discription</u>													
<p>ARITHMETIC BOARD. For use an ALL 4000 series processors excluding 4060.          This board contains the M1 multiplexer, M2 multiplexer and the Function Unit together with its carry logic.          It also contains the Process Counter and a multiplexer for routing data from the IPL assemblies to the mill.</p>													
<u>Position</u>													
Dedicated to POSITION 5 of the Central Processor crate.													
<u>Patching</u>													
Ready/Way	READY		WAY										
None	<table border="1"> <tr><td colspan="2">Court Area</td></tr> <tr><td> </td><td> </td></tr> </table>		Court Area				<table border="1"> <tr><td colspan="2">Court Area</td></tr> <tr><td> </td><td> </td></tr> </table>			Court Area			
Court Area													
Court Area													
Other	<p>IPL Rom 's will be found fitted to EC5 and EC6 edge connectors.</p> <p>IPL ROM assy. fitted to EC5 is loaded when IPL1 key is depressed</p> <p>IPL ROM assy fitted to EC6 is loaded when IPL2 key is depressed.</p>												
<u>examples</u>													
IPLA14 - Cart Disc or IPLA15 - LMHb/Cart Disc		IPLA11 - Paper Tape or IPLA16 - Cassette											
<u>EDGE CONNECTERS</u>													
EC1	EC2	EC3	EC4	EC5	EC6								
				Usually IPL11 or IPLA16	Usually IPLA14 or IPLA15								
<u>Alternatives</u>													
None													



BOARD NUMBER	105	EPL NUMBER	ALL 4080 & 4070				CATALOGUE NUMBER	B1723								
<u>Discription</u>	<p>MAIN REGISTER BOARD. For use on ALL 4000 series processors excluding 4060's.          This board contains the Main and Auxiliary registers in 74170 packages. (Multi-port Register Files). Also on the board is the logic controlling the writing of data into the files together with some monitoring logic.</p>															
<u>Position</u>	Dedicated to POSITION 6 of the Central Processor crate.															
<u>Patching</u>			READY				WAY									
Ready/Way	None		<table border="1"> <thead> <tr> <th colspan="2">Court Area</th> </tr> </thead> <tbody> <tr> <td></td> <td></td> </tr> </tbody> </table>		Court Area				<table border="1"> <thead> <tr> <th colspan="2">Court Area</th> </tr> </thead> <tbody> <tr> <td></td> <td></td> </tr> </tbody> </table>		Court Area					
Court Area																
Court Area																
Other	None															
<u>EDGE CONNECTERS</u>																
	EC1	EC2	EC3	EC4	EC5	EC6										
<u>Alternatives</u>	NONE															


BOARD NUMBER	106	EPL NUMBER	ALL 4080 & 4070				CATALOGUE NUMBER	B1724
<u>Discription</u>	<p>SHIFTER BOARD. For use in ALL 4000 series central processors excluding 4060.          This board contains the F and G latches and M3 and M4 multiplexers, that form the shifter. The board contains logic to cope with end effects when shifting registers longer than 16 bits and some conditions logic.</p>							
<u>Position</u>	Dedicated to POSITION 7 of the Central Processor crate.							
<u>Patching</u>			READY			WAY		
Ready/Way			Court Area			Court Area		
	None							
Other	None							
<u>EDGE CONNECTERS</u>								
	EC1	EC2	EC3	EC4	EC5	EC6		
<u>Alternatives</u>	NONE							

BOARD NUMBER 107	EPL NUMBER CB80/01, 02.	CATALOGUE NUMBER B1725								
<u>Description</u> EXPONENT BOARD. For use on 4080-CB80/01 and 02 CPU crates. Contains process mill and CPU registers. Also generates Command Interface for IOP's.										
<u>Position</u> Dedicated to POSITION 8 of CPU crate.										
<u>Patching</u> Ready/Way	READY	WAY								
	None									
Other	<table border="1"> <tr> <td colspan="2">Court Area</td> <td colspan="2">Court Area</td> </tr> <tr> <td></td> <td></td> <td></td> <td></td> </tr> </table> <p>Wire link fitted in court area B2 (Interlock). This link must be fitted if there is no IOP connected to the board, otherwise an interlock condition will exist. If an IOP is fitted then remove link.</p>		Court Area		Court Area					
Court Area		Court Area								
<p>NOTE: Command Interface cable (67-08270K1) is fitted to EC6 when using EMC's. It may go directly to Bd.131 in the EMC if there is only 1 or 2 EMC's fitted. Otherwise it may go via Command Interface extender Bds.186,187.</p>										
<u>EDGE CONNECTERS</u>										
EC1	EC2	EC3	EC4	EC5	EC6					
					To EMC's					
<u>Alternatives</u> Board 183 may be used without restriction.										

BOARD NUMBER	108	EPL NUMBER	CB 80/01, 02, CB 70/03, 04, 05.	CATALOGUE NUMBER	B1726												
<u>Description</u> ADDRESS MAPPING UNIT (AMU) BOARD. The function of this board is to generate absolute store addresses and to check that the addresses are valid.  To be used on 4080-CB80/01 & 02 4080-CB70/05 4070-CB70/03 and 4082-CB70/04																	
<u>Position</u>  Dedicated to POSITION 9 of CPU crate.																	
<u>Patching</u>	Ready/Way	READY		WAY													
		<table border="1"> <tr> <td colspan="2">Court Area</td> </tr> <tr> <td></td> <td></td> </tr> </table>		Court Area				<table border="1"> <tr> <td colspan="2">Court Area</td> </tr> <tr> <td></td> <td></td> </tr> </table>		Court Area							
Court Area																	
Court Area																	
Other	None																
NB. In an emergency Bd.141 may be used as an alternative but a Bd.143 (timer) must be fitted with it. Excluding CB80/01 & 02  ANY ALTERNATIVES SHOULD ONLY BE FITTED ON A TEMPORARY BASIS.																	
<u>EDGE CONNECTERS</u>																	
<table border="1"> <tr> <td>EC1</td> <td>EC2</td> <td>EC3</td> <td>EC4</td> <td>EC5</td> <td>EC6</td> </tr> <tr> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> </tr> </table>						EC1	EC2	EC3	EC4	EC5	EC6						
EC1	EC2	EC3	EC4	EC5	EC6												
<u>Alternatives</u> Bd.141 (see above for limitations)																	

BOARD NUMBER	109	EPL NUMBER	ALL 4080 & 4070			CATALOGUE NUMBER	B1727											
<u>Description</u>	<p>INSTRUCTION DECODE BOARD. For use on the whole range of 4000 series machines. (excluding 4060)</p> <p>This board decodes instructions and provides information concerning the current and the next instruction to the control logic. This information enables the microprogram and control logic to form operand addresses and execute instructions.</p>																	
<u>Position</u>	Dedicated to POSITION 10 of the CPU crate.																	
<u>Patching</u>			READY		WAY													
Ready/Way	None		<table border="1"> <thead> <tr> <th>Court</th> <th>Area</th> </tr> </thead> <tbody> <tr> <td></td> <td></td> </tr> </tbody> </table>	Court	Area			<table border="1"> <thead> <tr> <th>Court</th> <th>Area</th> </tr> </thead> <tbody> <tr> <td></td> <td></td> </tr> </tbody> </table>	Court	Area								
Court	Area																	
Court	Area																	
Other	None																	
<u>EDGE CONNECTERS</u>																		
<table border="1"> <thead> <tr> <th>EC1</th> <th>EC2</th> <th>EC3</th> <th>EC4</th> <th>EC5</th> <th>EC6</th> </tr> </thead> <tbody> <tr> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> </tr> </tbody> </table>							EC1	EC2	EC3	EC4	EC5	EC6						
EC1	EC2	EC3	EC4	EC5	EC6													
<u>Alternatives</u>	NONE																	

BOARD NUMBER 110	EPL NUMBER C880/01, 02, CB70/03, 04, 05.	CATALOGUE NUMBER B1728								
<u>Discription</u> CONTROL AND MONITOR UNIT (CMU) BOARD. For use on 4080-CB80/01; 4080-CB80/02; 4080-CB70/05; 4070-CB70/03; and 4082-CB70/04. Provides the interface between the lamps and keys on the front panel and the monitoring logic.										
<u>Position</u> Dedicated to POSITION 11 in the CPU crate.										
<u>Patching</u>	READY	WAY								
Ready/Way	<table border="1"> <tr><td colspan="2">Court Area</td></tr> <tr><td> </td><td> </td></tr> </table>	Court Area				<table border="1"> <tr><td colspan="2">Court Area</td></tr> <tr><td> </td><td> </td></tr> </table>	Court Area			
Court Area										
Court Area										
Other	None									
<p><u>NB.</u> CMU board 142 <u>CANNOT</u> be used as a replacement on any of the above crates.</p>										
<u>EDGE CONNECTERS</u>										
EC1	EC2	EC3	EC4	EC5	EC6					
			Cable to Front Panel	Cable to Front Panel	Cable to Front Panel					
<u>Alternatives</u>			NONE							

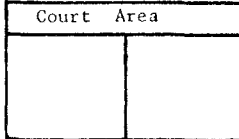
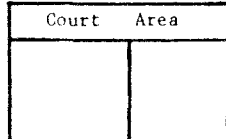
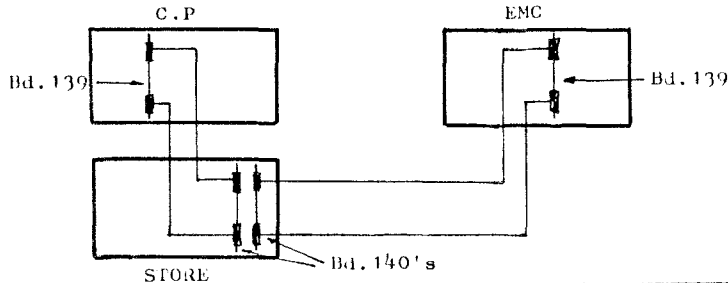
BOARD NUMBER	111	EPL NUMBER	CB80/01, 02.			CATALOGUE NUMBER	B1729											
<u>Discription</u>	<p>TIMER BOARD. For use on 4080 Central Processor Units only Type CB80/01 and CB80/02</p> <p>This board contains the basic timer and generates clock signals for the CPU and BMC.Store interface priority and error reporting logic is also held on this board.</p> <p>( SEE INFO ON BOARDS 138 &amp; 143 )</p>																	
<u>Position</u>	Dedicated to POSITION 12 in the Central Processor crate.																	
<u>Patching</u>	READY		WAY															
Ready/Way	None		<table border="1"> <tr><td colspan="2">Court Area</td></tr> <tr><td> </td><td> </td></tr> </table>	Court Area				<table border="1"> <tr><td colspan="2">Court Area</td></tr> <tr><td> </td><td> </td></tr> </table>		Court Area								
Court Area																		
Court Area																		
Other	<p>D.I.L. Switch fitted in court area B1, normally set to mid position but may be moved left or right for fast or slow operation to test margins.</p> 																	
<p>N.B. Board 138 may be used as a direct alternative without the need for any modification.</p> <p>Bd.143 may be used withthe following conditions;</p> <p>Boards 141 and 143 must be used as a pair. One wire to be added on CB80/01 backplane (M.I. ) to facilitate their use.</p> <p>The addition of this wire does not prevent reversion to Bds.108,111 or 138.</p> <p>Bds.141,143 correct the PROT. ERROR lamp function.</p>																		
<u>EDGE CONNECTERS</u>																		
<table border="1"> <tr> <td>EC1</td> <td>EC2</td> <td>EC3</td> <td>EC4</td> <td>EC5</td> <td>EC6</td> </tr> <tr> <td> </td> <td> </td> <td> </td> <td> </td> <td> </td> <td> </td> </tr> </table>							EC1	EC2	EC3	EC4	EC5	EC6						
EC1	EC2	EC3	EC4	EC5	EC6													
<u>Alternatives</u>																		
<p>Bd.138 or Bd.143 (See above for limitations.</p> <p>Any alternatives used, should be on a temporary basis only.</p>																		

BOARD NUMBER	112	EPL NUMBER	CB80/01, 02, CB70/03, 04, 05.			CATALOGUE NUMBER	B1730							
<u>Discription</u>	<p>BMC CONTROL BOARD. One of 3 boards forming the BMC on 4000 series systems contained within the CP crate. Other boards are 113 and 114.</p> <p>Bd.112 is fitted as standard on 4080 - CB80/01, CB80/02, CB70/05 4070 - CB70/03 and 4082 - CB70/04</p> <p>Bd. contains Control, Microprogram and some N.I. logic.</p>													
<u>Position</u>	Dedicated to POSITION 13 of Central Processor crate.													
<u>Patching</u>		READY		WAY										
Ready/Way	None	<table border="1"> <thead> <tr> <th>Court</th> <th>Area</th> </tr> </thead> <tbody> <tr> <td></td> <td></td> </tr> </tbody> </table>		Court	Area			<table border="1"> <thead> <tr> <th>Court</th> <th>Area</th> </tr> </thead> <tbody> <tr> <td></td> <td></td> </tr> </tbody> </table>		Court	Area			
Court	Area													
Court	Area													
Other	None													
<p>N.B. In an emergency Bd.144 may be used as an alternative. This is a modified board 112 which contains an I/O ERROR lamp correction.</p> <p>If Bd.144 is used as a replacement the correct board should be refitted when available.</p>														
<u>EDGE CONNECTERS</u>														
	EC1	EC2	EC3	EC4	EC5	EC6								
<u>Alternatives</u>	Bd.144 ( See note above)													



BOARD NUMBER	113	EPL NUMBER	ALL 4080 & 4070	CATALOGUE NUMBER	B1731																																																			
<u>Description</u>	<p>BMC REGISTER 2 BOARD. One of 3 boards forming the BMC on All 4000 Systems (Excluding 4060). Other two boards are 114 and 112 (or 144)</p> <p>The function of the BMC is to perform data transfers between I/O devices and Main Store. The BMC is controlled by the Command interface from the CP.</p> <p>BQ.113 contains the Most Significant part of the registers and function unit and some of the N.I. control logic.</p>																																																							
<u>Position</u>	Dedicated to POSITION 14 in the Central Processor																																																							
<u>Patching</u>	READY		WAY																																																					
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Other	<p>None</p> <p>Dual-in-Line switch fitted in Court Area A5 (or B2) to set the Way Range of the board. Normally set to Maximum.</p> <table border="1"> <thead> <tr> <th>Highest way in use.</th> <th>Range</th> <th>DIL Switch Setting</th> </tr> </thead> <tbody> <tr><td>0- 15</td><td>0</td><td>2, 4, 6, 8</td></tr> <tr><td>16- 31</td><td>1</td><td>1, 4, 6, 8</td></tr> <tr><td>32- 47</td><td>2</td><td>2, 3, 6, 8</td></tr> <tr><td>48- 63</td><td>3</td><td>1, 3, 6, 8</td></tr> <tr><td>64- 79</td><td>4</td><td>2, 4, 5, 8</td></tr> <tr><td>80- 95</td><td>5</td><td>1, 4, 5, 8</td></tr> <tr><td>96-111</td><td>6</td><td>2, 3, 5, 8</td></tr> <tr><td>112-127</td><td>7</td><td>1, 3, 5, 8</td></tr> <tr><td>128-143</td><td>8</td><td>2, 4, 6, 7</td></tr> <tr><td>144-159</td><td>9</td><td>1, 4, 6, 7</td></tr> <tr><td>160-175</td><td>10</td><td>2, 3, 6, 7</td></tr> <tr><td>176-191</td><td>11</td><td>1, 3, 6, 7</td></tr> <tr><td>192-207</td><td>12</td><td>2, 4, 5, 7</td></tr> <tr><td>208-223</td><td>13</td><td>1, 4, 5, 7</td></tr> <tr><td>224-239</td><td>14</td><td>2, 3, 5, 7</td></tr> <tr><td>240-255</td><td>15</td><td>1, 3, 5, 7</td></tr> </tbody> </table>					Highest way in use.	Range	DIL Switch Setting	0- 15	0	2, 4, 6, 8	16- 31	1	1, 4, 6, 8	32- 47	2	2, 3, 6, 8	48- 63	3	1, 3, 6, 8	64- 79	4	2, 4, 5, 8	80- 95	5	1, 4, 5, 8	96-111	6	2, 3, 5, 8	112-127	7	1, 3, 5, 8	128-143	8	2, 4, 6, 7	144-159	9	1, 4, 6, 7	160-175	10	2, 3, 6, 7	176-191	11	1, 3, 6, 7	192-207	12	2, 4, 5, 7	208-223	13	1, 4, 5, 7	224-239	14	2, 3, 5, 7	240-255	15	1, 3, 5, 7
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<u>Alternatives</u>	NONE !!																																																							

BOARD NUMBER	114	EPL NUMBER	ALL 4080 & 4070	CATALOGUE NUMBER	B1732												
<u>Discription</u>	<p>BMC REGISTER 1 BOARD. One of 3 boards in the CP crate of ALL 4000 systems (excluding 4060) forming the Basic Multiplexer Channel. Other two boards are 112 (or 144) and 113.</p> <p>This board contains the Least Significant part of the registers and function unit, and the Command Interface control logic.</p> <p>The purpose of the Bmc is to perform Data transfers between peripheral and store.</p>																
<u>Position</u>	Dedicated to POSITION 15 in the Central Processor.																
<u>Patching</u>	READY		WAY														
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<p>Typical 4000 Configuration</p>																	
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EC1	EC2	EC3	EC4	EC5	EC6												
<u>Alternatives</u>	None																

BOARD NUMBER 139	EPL NUMBER CE04/02,05,08	CATALOGUE NUMBER B4939												
<u>Discription</u> STORE LINK BOARD. For use on 4000 series systems to provide a link for the store interface from the CP to Store Crates. Used in conjunction with a Port Bd. in Store crate. May be Bd.133,140 or 162. A lso used to provide link from EMC to Store (Not EDC) Link for 4070 Store Bd.134's or Semicon Bd.203's Not 4080 store Maximum of 4 boards in C.P. or EMC crate														
<u>Position</u> Dedicated to Slots 16 to 19 in C.P. crate and 3 to 6 if used in EMC crate														
<u>Patching</u> Ready/Way  Other	READY Court Area 	WAY Court Area 												
														
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			To EC4 of Store Port Bd.		To EC6 of Store Port Bd.									
<u>Alternatives</u> See page 194 for permissible Port/Link combinations.														

BOARD NUMBER 141	EPL NUMBER <b>CB70/01, 02, 06.</b>		CATALOGUE B4941 NUMBER														
<p><u>Description</u> ADDRESS MAPPING UNIT (AMU) BOARD. The function of this board is to generate absolute store addresses and to check that the addresses are valid.</p> <p>To be used on 4070-CB70/01 4082-CB70/02 4085-CB70/06</p>																	
<p><u>Position</u></p> <p>Dedicated to POSITION 9 of the CPU crate.</p>																	
<p><u>Patching</u></p> <p>Ready/Way</p>	<p>READY</p> <table border="1" data-bbox="662 561 900 697"> <tr> <th colspan="2">Court Area</th> </tr> <tr> <td></td> <td></td> </tr> </table> <p>None</p>		Court Area				<p>WAY</p> <table border="1" data-bbox="1108 561 1329 697"> <tr> <th>Court</th> <th>Area</th> </tr> <tr> <td></td> <td></td> </tr> </table>			Court	Area						
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<p><u>Alternatives</u> BD. 108 may <u>NOT</u> be used as an alternative.</p>																	



BOARD NUMBER	143	EPL NUMBER	CB70/01, 02, 06		CATALOGUE NUMBER	B4943								
<u>Description</u>	<p>TIMER BOARD. For use on 4070-CB70/01; 4082-CB70/02 and 4085-CB70/06 Central Processors.</p> <p>This board contains the basic timer and generates clock signals for the CPU and BMC. Store interface priority and error reporting logic is also held on this board.</p>													
<u>Position</u>	Dedicated to POSITION 12 in Central Processor crate.													
<u>Patching</u>		READY		WAY										
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<u>Alternatives</u>	NONE													



